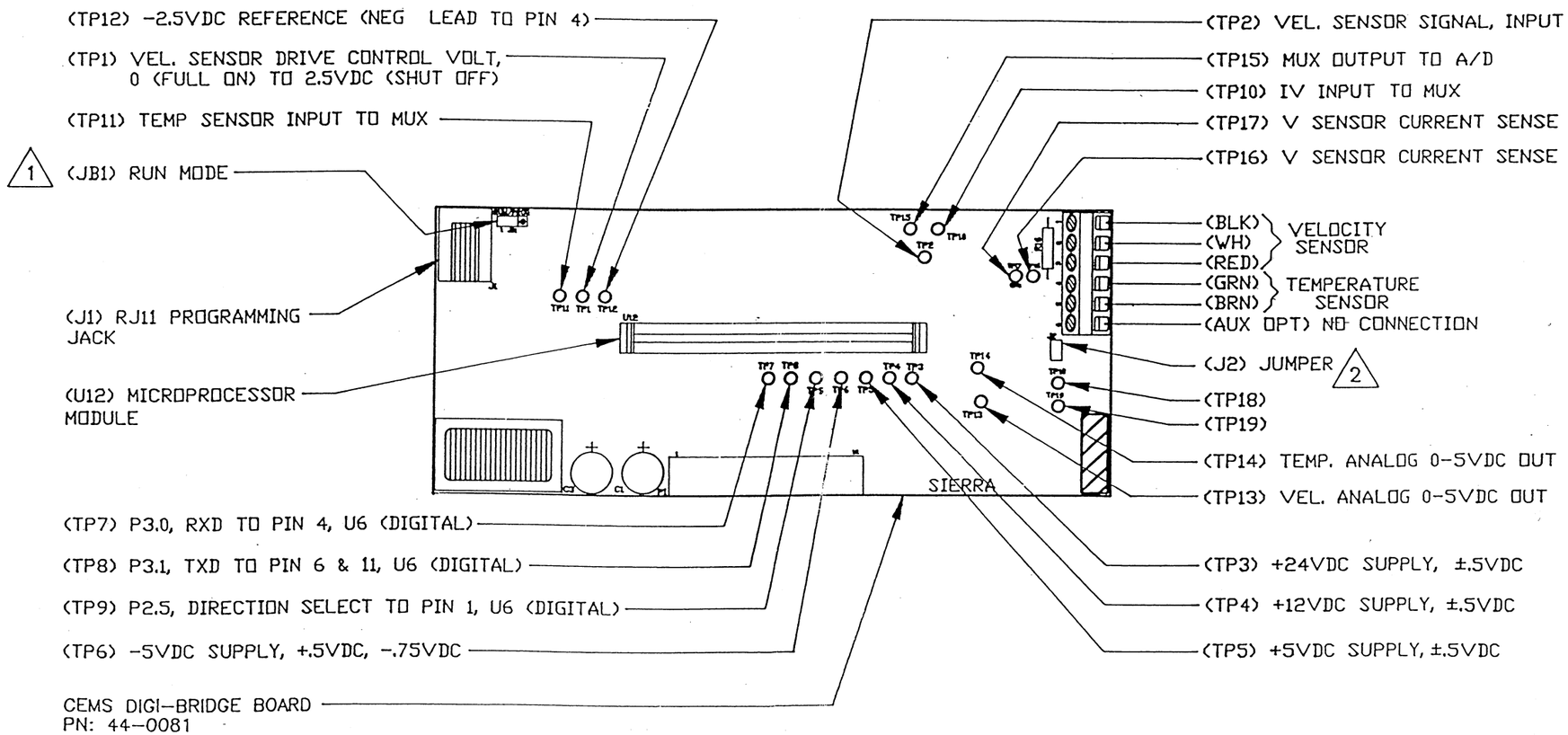
Series 670 Digital Flow Averaging Array

Digi-Bridge Board Critical Component Locations

E

NOTES:

1. JB1: RUN/PROGRAM JUMPER. MUST BE IN RUN FOR NORMAL OPERATION. JUMPING MOMENTARY TO PROGRAM AND BACK TO RUN WILL RESET THE MICROPROCESSOR.
2. J2: FOR CALIBRATION PURPOSE ONLY. JUMPER MUST BE JUMPERED FOR NORMAL OPERATION. REMOVING JUMPER WILL NOT HARM SYSTEM AND IS USEFUL ON THE BENCH TO PREVENT GETTING BURNED FROM THE HOT VELOCITY SENSOR.
3. 'TP' DESIGNATION INDICATE "TEST POINT."



Series 670 Digital Flow Averaging Array

Controller Board Critical Component Locations F

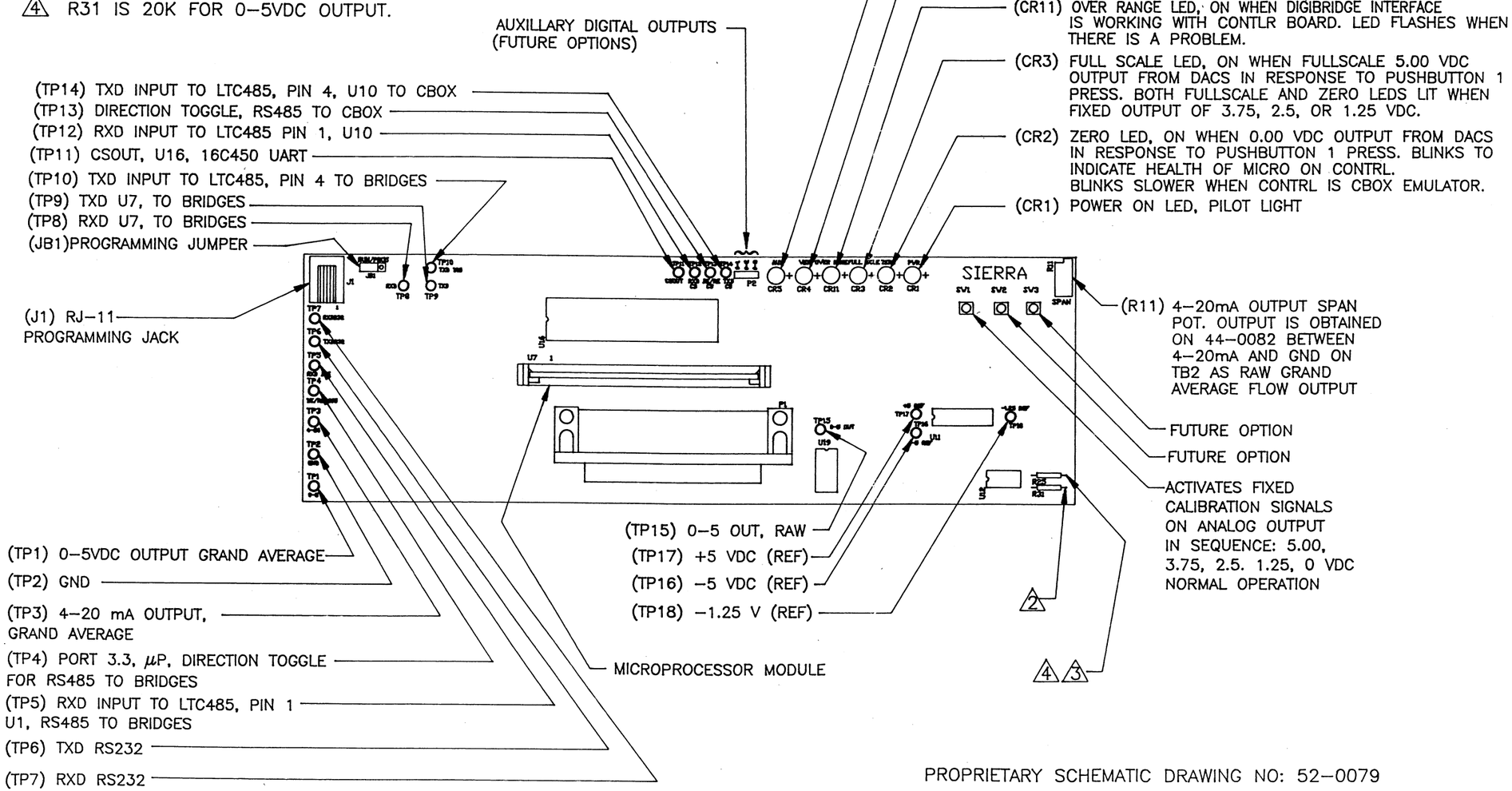
NOTES:

1. 'TP' DESIGNATION INDICATES "TEST POINT."
   'CONTRL' ABBREVIATION INDICATES "CONTROLLER"
   'CBOX' ABBREVIATION INDICATES "COLLECTOR BOX"

2. REMOVE R25 FOR 0–1VDC OUTPUT.

3. CHANGE R31 TO 45.3K FOR 1 TO 10 VDC OUTPUT.

4. R31 IS 20K FOR 0–5VDC OUTPUT.



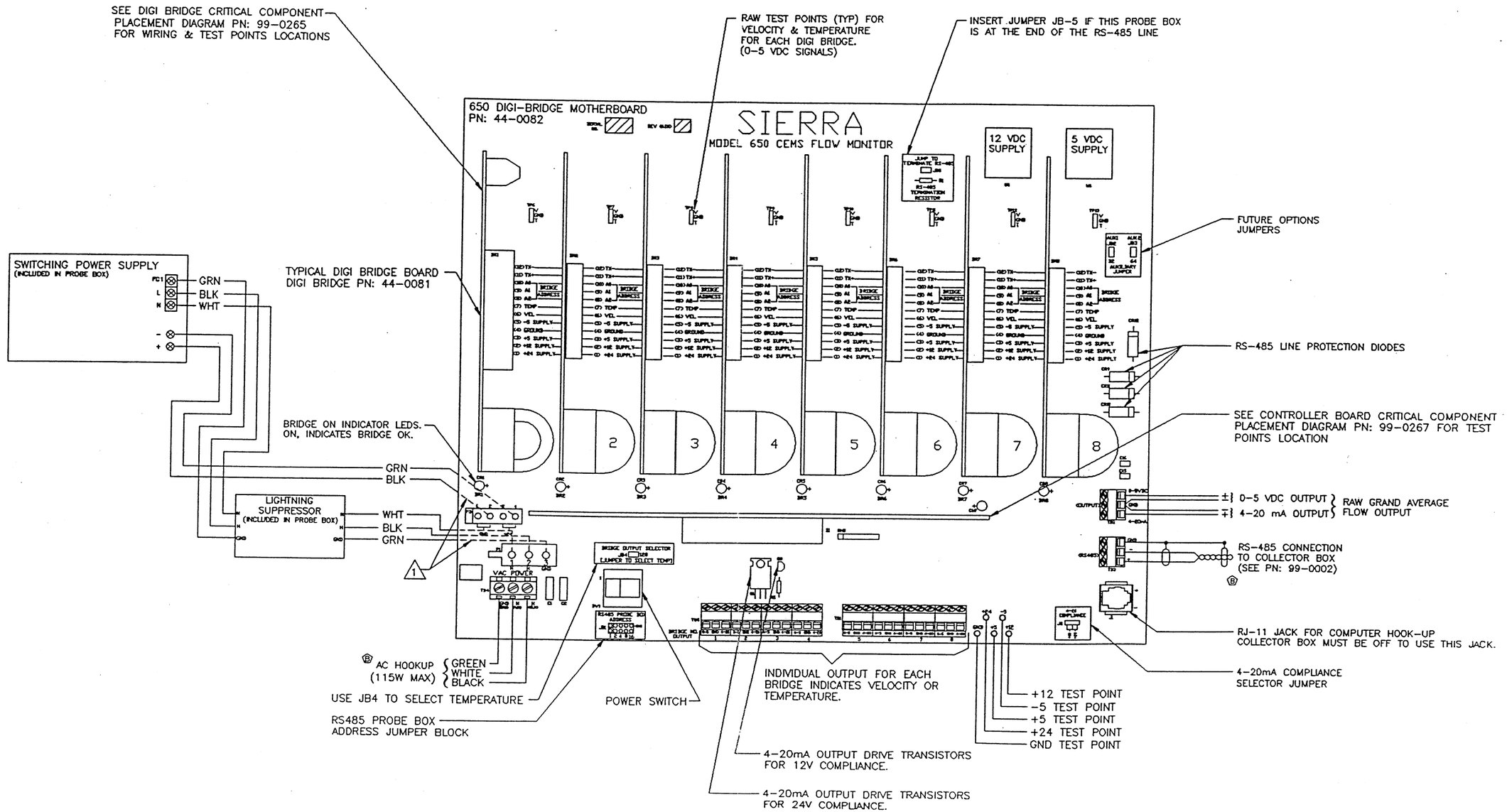
PROPRIETARY SCHEMATIC DRAWING NO: 52–0079

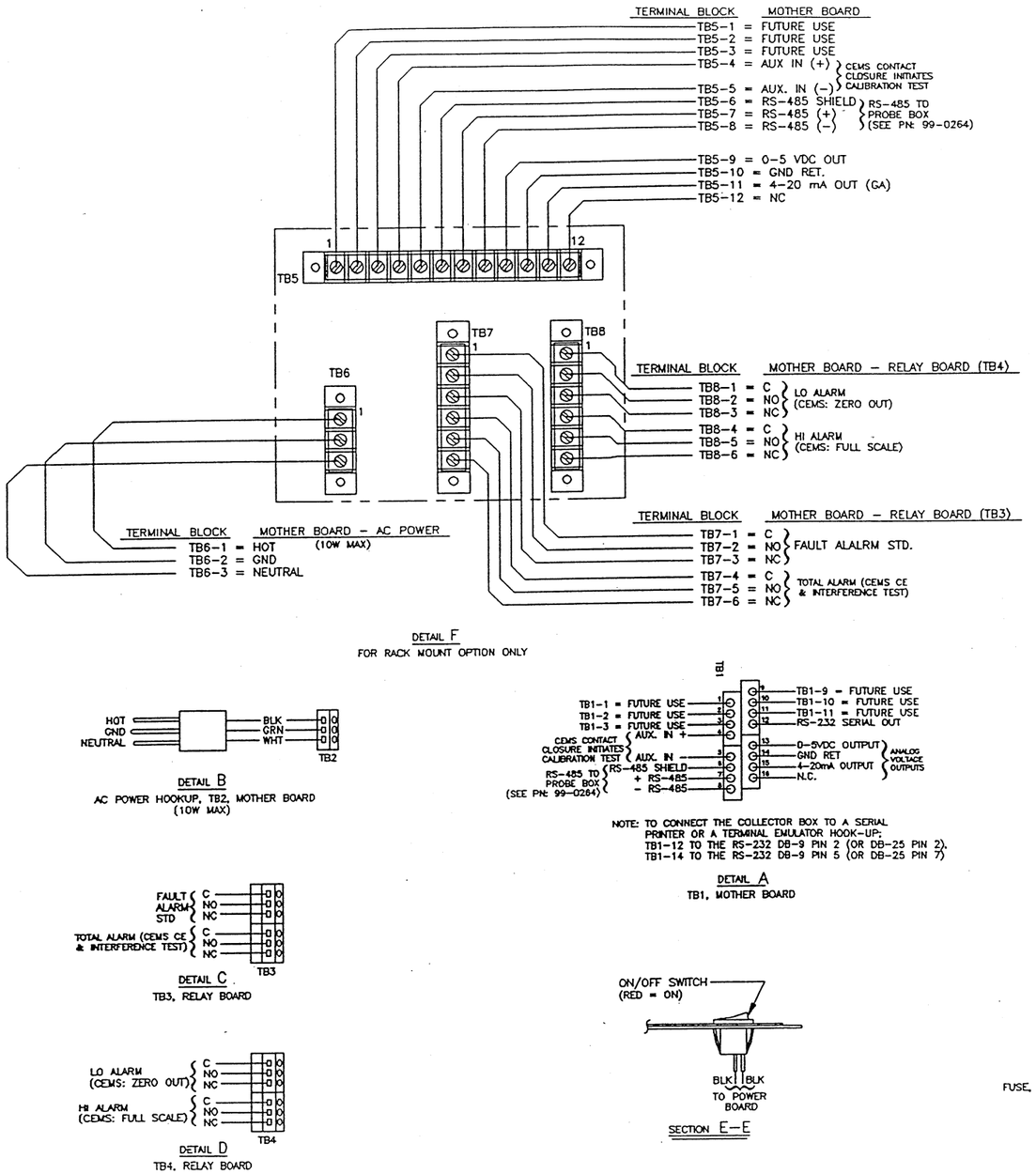
Series 670 Digital Flow Averaging Array

Probe-Box Motherboard Critical Component Locations G

NOTES:

1. CONNECT WIRES TO THE BOTTOM SIDE OF BOARD.





Collector-Box Motherboard Critical Component Locations H